

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	LOGIC CIRCUITS HAVING LINEAR AND CELLULAR GATE TRANSISTORS	
Application Number :		
Date :		
First Named Applicant:	Victor Wing Chung Chan	
Confirmation Number:		
Attorney Docket Number:	FIS920040113US1	
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Documents being submitted:	Files
us-assignment	FIS920040113US1-usassn.xml us-assignment.xsl us-assignment.dtd 20040113assgn1.tif 20040113assgn2.tif
us-request	FIS920040113US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	FIS920040113US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	20040113dec1.tif
us-declaration	20040113dec2.tif
us-declaration	20040113dec3.tif
application-body	FIS920040113US1-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd 04113form1.tif 04113form2.tif 04113dwg1.tif 04113dwg2.tif 04113dwg3.tif 04113dwg4.tif 04113dwg5.tif

Comments

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